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10/075750



PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10075750	FILING DATE 02/14/2002	CLASS 331	SUBCLASS 74	GAU 281	EXAMINER COX
**APPLICANTS: Gauthier Claude; Amick Brian; Liu Dean; Trivedi Pradeep;					
**CONTINUING DATA VERIFIED: None <i>are</i>					
<p>BEST AVAILABLE COPY</p>					
** FOREIGN APPLICATIONS VERIFIED: None <i>are</i>					
PG-PUB DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO			
35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		03226.170001;P7188			
Verified and Acknowledged Examiners's initials <i>are</i>					
TITLE : Jitter estimation for a phase locked loop					
U.S. DEPT. OF COM. / PAT. & TM. PTO-4350 (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig. <i>4</i>	
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	
		Application Examiner	
PREPARED FOR ISSUE			
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